

WORLD INTELLECTUAL PROPERTY ORGANIZATION International Bureau



INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification 5:
H02H 5/04

A1

(11) International Publication Number: WO 94/00901
(43) International Publication Date: 6 January 1994 (06.01.94)

(21) International Application Number:

PCT/US92/10547

(22) International Filing Date:

4 December 1992 (04.12.92)

(30) Priority data:

901,320

19 June 1992 (19.06.92)

US

(71) Applicant: SQUARE D COMPANY [US/US]; 1415 South Roselle Road, Palatine, IL 60067 (US).

(72) Inventor: DVORAK, Robert, F.; 206 Candlestick Drive, Mount Vernon, IA 52314 (US).

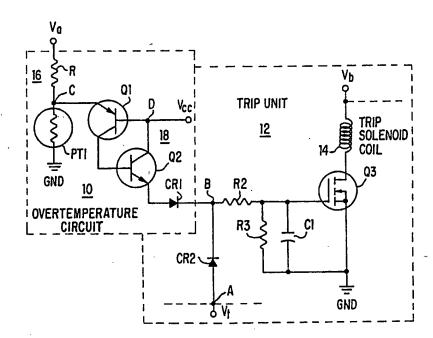
(74) Agent: GOLDEN, Larry, I.; General Patent Counsel, Square D Company, 1415 South Roselle Road, Palatine, IL 60067 (US).

(81) Designated States: AU, CA, JP, European patent (AT, BE, CH, DE, DK, ES, FR, GB, GR, IE, IT, LU, MC, NL, DT, SE)

Published

With international search report.

(54) Title: OVERTEMPERATURE SENSING AND SIGNALING CIRCUIT



(57) Abstract

An overtemperature sensing and signaling circuit for electronic trip units includes a voltage divider (16) and a trigger circuit (18). The voltage divider is formed of a first resistor (R1) and a positive coefficient resistor (PT1). The trigger circuit is formed of a first bipolar transistor of the PNP-type conductivity and a second bipolar transistor (Q2) of the NPN-type conductivity.

FOR THE PURPOSES OF INFORMATION ONLY

Codes used to identify States party to the PCT on the front pages of pamphlets publishing international applications under the PCT.

ΑT	Austria	FR	France	MR	Mauritania
AU	Australia	GA	Gabon	MW	Malawi
BB	Barbados	GB	United Kingdom	NE	Niger
BE	Belgium	GN	Guinea	NL	Netherlands
BF	Burkina Faso	GR	Greece	NO	Norway
BG	Bulgaria	HU	Hungary	NZ	New Zealand
BJ	Benin	1E	Ireland	PL	Poland
BR	Brazil	IT	Italy	PT	Portugal
BY	Belarus	JP	Japan -	RO	Romania
CA	Canada	KР	Democratic People's Republic	RU	Russian Federation
CF	Central African Republic		of Korea	SD	Sudan
CG	Congo	KR	Republic of Korea	SE	Sweden
CH	Switzerland	ΚZ	Kazakhstan	SI	Slovenia
Ci	Côte d'Ivoire	Li	Liechtenstein	SK	Slovak Republic
CM	Cameroon	LK	Sri Lanka	SN	Senegal
CN	China	LU	Luxembourg	TD	Chad
CS	Cechoslovakia	ĹŸ	Latvia	TG	Togo
CZ	Czech Republic	MC	Monaco	UA	Ukraine
DE	Germany	MG	Madagascar	US	United States of America
DK	Denmark	· ML	Mali	UZ	Uzbekistan
ES	Spain	MN	Mongolia	VN	Vict Nam
P!	Finland				

WO 94/00901 PCT/US92/10547

5

10

15

20

OVERTEMPERATURE SENSING AND SIGNALING CIRCUIT

BACKGROUND OF THE INVENTION

This invention relates generally to circuit interrupters having electronic trip units and more particularly, it relates to an overtemperature sensing and signaling circuit employed in circuit interrupters with electronic trip units for protecting the trip units from operating at an excessively high ambient temperature.

As is generally well-known in the art, circuit breakers have been widely used in commercial and industrial applications for protecting electrical conductors and apparatus connected thereto from damage due to excessive current flow. Circuit breakers typically included trip systems which were designed to interrupt when the current flowing through them exceeded a predetermined level. Specifically, most simple trip systems utilized an electromagnet to trip the circuit in response to abnormal current or voltage. The electromagnet provided a magnetic field in response to current flowing through the circuit breaker. When the current level increased beyond the predetermined level or trip point,

10

15

the magnetic field "trips" a mechanism which causes a set of circuit breaker contacts to release, thereby "opening" or "breaking" the circuit path.

As tripping systems became more sophisticated and elaborate to meet the demands by industry for improved accuracy and flexibility, there were developed heretofore circuit interrupters which utilized electronic control circuits in the trip unit. Since these circuit interrupters were generally mounted in a molded case or housing, the ambient temperature could possibly increase beyond the rating of some of the electronic components used in the trip unit. This increased ambient temperature could be caused by the heat generated by the current being passed through the circuit interrupter. In any event, it is known that operating such electronic components beyond their rated temperatures will decrease significantly their product life-time and could even cause premature failure of the trip unit.

Accordingly, there has arisen a need for providing
overtemperature protection for such circuit interrupters
having electronic trip units. In the past, it was known
in the prior art to provide thermally activated switches

10

15

20

disposed within the housing enclosing the circuit interrupter and connected to the internal conductors thereof.

Each of the thermal switches was formed of a bimetal
element which closed the switch contacts when the
temperature of the associated conductors rose above a
predetermined temperature. When the conductors overheat,
the trip unit connected in parallel with these switches
caused the trip coil to energize for breaking the
contacts of the circuit interrupter. Although these
bimetal thermally activated switches achieved the
protection function satisfactorily, they suffer from the
disadvantages of being relatively large in size and high
in cost.

Thus, the present invention is directed to an improved overtemperature sensing and signaling circuit which provides for more reliable operation and performance at a reduced cost. In particular, the overtemperature sensing and signaling circuit of the instant invention includes a voltage divider and a trigger circuit. The voltage divider is formed of a first resistor and a positive coefficient resistor. The trigger circuit is formed of a first bipolar transistor of the PNP-type conductivity and a second bipolar transistor of the NPN-type conductivity.

10

15

SUMMARY OF THE INVENTION

Accordingly, it is a general object of the present invention to provide an improved overtemperature sensing and signaling circuit for electronic trip units which is relatively simple and economical to manufacture and assemble, but yet overcomes the disadvantages of the prior art protection circuits.

It is an object of the present invention to provide an improved overtemperature sensing and signaling circuit for electronic trip units which has a high reliability in its operation.

It is another object of the present invention to provide an improved overtemperature sensing and signaling circuit for electronic trip units which is formed of components with relatively low cost.

It is still another object of the present invention to provide an improved overtemperature sensing and signaling circuit for electronic trip units which

10

15

20

includes a voltage divider having a positive coefficient resistor and a trigger circuit formed of a first bipolar transistor of the PNP-type conductivity and a second bipolar transistor of the NPN-type conductivity.

In accordance with these aims and objectives, the present invention is concerned with the provision of an improved overtemperature sensing and signaling circuit for electronic trip units which includes a voltage divider and a trigger circuit. The voltage divider is formed of a first resistor and a positive coefficient resistor. The positive coefficient resistor is used to sense the ambient temperature within a circuit interrupter housing and is connected between a first node and a ground potential. The first resistor has its one end connected to a first power supply potential and its other end connected to the first node.

The trigger circuit is formed of a first switching transistor and a second switching transistor. The first switching transistor has its emitter connected to the first node. The second switching transistor has its base connected to the collector of the first switching transistor, its collector connected to the base of the first

10

15

20

switching transistor and to a second power supply potential, and its emitter coupled to a second node.

BRIEF DESCRIPTION OF THE DRAWING

These and other objects and advantages of the present invention will become more fully apparent from the following detailed description when read in conjunction with the accompanying drawing in which there is shown a schematic circuit diagram of the overtemperature sensing and signaling circuit for carrying out the protection feature of the instant invention.

DESCRIPTION OF THE PREFERRED EMBODIMENT

Referring now in detail to the drawing of the particular illustration, there is shown a schematic circuit diagram of an improved overtemperature sensing and signaling circuit 10 of the present invention for use in association with an electronic trip unit 12 employed in a solid-state circuit interrupter. The overtemperature sensing and signaling circuit 10 serves to protect the trip unit from operating at an excessively high ambient temperature which could possibly shorten the

10

15

20

product life-time of the various electronic components used in the trip unit. Thus, the overtemperature sensing and signaling circuit is designed to energize a trip solenoid coil 14 for opening contactors so as to break the current path in the circuit interrupter when the ambient temperature in a housing enclosing the circuit interrupter rises above a predetermined level.

It should be noted that only the pertinent portions of the trip unit 12 which is necessary for the understanding of the operation of the present invention has been depicted in the drawing. It will also be understood that the trip unit 12 itself does not form any part of the present invention. Therefore, for the sake of clarity, many of the components of the trip unit 12 have been intentionally omitted from the drawing.

The trip unit 12 is shown to include a power N-channel field-effect transistor Q3 for energizing the trip solenoid coil 14. One end of the solenoid coil 14 is connected to a voltage source V_b, which is typically between +10.0 to +20.0 VDC. The other end of the solenoid coil is connected to the drain electrode of the transistor Q3. The source electrode of the transistor Q3

15

20

25

is connected to a ground potential GND. The resistor R2 and the capacitor C1 form a low-pass filter to reduce high frequency noise which could false trigger the transistor Q3. The resistor R3 provides a shunt to ground such that leakage current do not false trigger the transistor Q3. A trip voltage V, is generated at node A by portions of the trip unit circuitry that are not shown. The trip voltage V, is only generated under over-current conditions of sufficient magnitude and duration which indicates that a fault has occurred. This trip voltage V, is applied to the anode of the diode CR2 whose cathode is connected to the node B.

Under normal operating conditions, the trip voltage V_t is near the ground potential and the transistor Q3 is thus rendered to be non-conductive. As a result, the trip solenoid coil 14 will not be energized and the contactors of the circuit interrupter will remain closed or latched, thereby allowing current to flow therethrough. However, when a fault is sensed by the trip unit circuitry, the trip voltage V_t will be applied to the gate of the transistor Q3 via the diode CR2 and the resistor R2. Consequently, the transistor Q3 will be rendered conductive so as to cause the trip solenoid coil 14 to be energized. Once energized, the solenoid coil proceeds to open or break the contactors in the circuit

10

15

20

interrupter and causes the current flowing in the internal conductors thereof to be interrupted.

In order to prevent the electronic components In the trip unit 12 from being damaged by being operated at beyond their rated temperatures, the overtemperature sensing and signaling circuit 10 of the instant invention is provided to generate an overtemperature trip signal for activating the solenoid coil so as to interrupt or break the current path. The overtemperature sensing and signaling circuit 10 is comprised of a voltage divider 16 and a trigger circuit 18.

The voltage divider is formed of a first resistor R1 and a second resistor PT1. One end of the first resistor is connected to a power source V, which is typically at +11.0 volts. The other end of the first resistor R1 is connected to one end of the second resistor PT1 and to an internal node C. The other end of the second resistor PT1 is connected to a ground potential GND. The second resistor PT1 is comprised of a positive coefficient resistor PT1 is comprised of a positive coefficient temperature increases, thereby increasing the voltage at the internal node C.

15

20

The trigger circuit 18 includes a bipolar PNP-type switching transistor Q1 and a bipolar NPN-type switching transistor Q2. The transistor Q1 has its emitter connected to the junction of the first and second resistors at the node C, its base connected to the collector of the transistor Q2, and its collector connected to the base of the transistor Q2. The junction of the base of the transistor Q1 and the collector of the transistor Q2 is further connected to a node D for receiving a power source $V_{\rm ee}$, which is typically at +5.0 volts.

A diode CR1 has its anode connected to the emitter of the transistor Q2 and its cathode connected to the second node B. The cathode of the diode CR1 provides an overtemperature trip signal for energizing the trip solenoid coil whenever the ambient temperature in the housing cavity exceeds a predetermined level.

The operation of the overtemperature sensing and signaling circuit 10 will now be explained. Under normal operating conditions, the temperature in the housing cavity containing the trip unit 12 will be within normal operating limits. Thus, the resistance value of the positive coefficient resistor PT1 will be relatively

10

15

20

small, i.e., typically in the range of 100 to 1500 ohms, dependent upon the actual ambient temperature. Under this condition, the voltage at the node C will be low and the emitter-base junction of the transistor Q1 will be reverse biased. As a result, the transistors Q1 and Q2 both will be turned off so that no emitter current will flow through the diode CR1. The power field-effect transistor Q3 will also be rendered non-conductive, thereby maintaining the contactors of the circuit interrupter closed.

Now, assume that the ambient temperature of the housing cavity is elevated above the sense temperature of the positive coefficient resistor PT1. The resistance value of the second resistor PT1 will then increase rapidly proportional to the degrees of temperature rise. When the emitter voltage defining a turn-on signal of the transistor Q1 exceeds the power source $V_{\rm cc}$ of +5.0 volts at its base by a voltage $V_{\rm bc}$, the transistor Q1 will be turned on so as to pull up the base of the transistor Q2. Consequently, the transistor Q2 will also be rendered conductive. When the transistor Q2 turns on, the overtemperature trip signal at the node B will be nearly equal to the power source $V_{\rm cc}$. This high voltage of the overtemperature trip signal will appear at the gate of

10

20

the transistor Q3 and turns on the same. This, in turn, will engage the trip solenoid coil 14, thereby interrupting the current path in the circuit interrupter so as to protect the trip unit from being operated at an excessively high ambient temperature.

For completeness in disclosure of the above described overtemperature sensing and signaling circuit but not for the purposes of limitation, the following representative values and component identifications are submitted. These values and components were employed in the preferred embodiment of the invention described herein.

	PART	TYPE or VALUE
	R1	10k ohms
15	PT1	MURATA ERIE Type PTH59F04BF222TS
	Q1 .	2N3906
	Q2	2N3904
	CR1	IN4148

From the foregoing detailed description, it can thus be seen that the present invention provides an improved

10

15

20

overtemperature sensing and signaling circuit for electronic trip units which includes a voltage divider and a trigger circuit. The voltage divider includes a positive coefficient resistor. The trigger circuit is formed of a first bipolar transistor of the PNP-type conductivity and a second bipolar transistor of the NPN-type conductivity. The overtemperature circuit of the present invention provides for more reliable operation and performance at reduced cost than those traditionally available.

while there has been illustrated and described what is at present considered to be a preferred embodiment of the present invention, it will be understood by those skilled in the art that various changes and modifications may be made, and equivalents may be substituted for elements thereof without departing from the true scope of the invention. In addition, many modifications may be made to adapt a particular situation or material to the teachings of the invention without departing from the central scope thereof. Therefore, it is intended that this invention not be limited to the particular embodiment disclosed as the best mode contemplated for carrying out the invention, but that the invention will include

all embodiments falling within the scope of the appended claims.

WHAT IS CLAIMED IS:

1. An overtemperature sensing and signaling circuit for electronic trip units comprising:

resistor (R1) and positive coefficient
resistance means (PT1) for sensing the ambient
temperature within a circuit interrupter
housing, said positive coefficient resistance
means being connected between a first node (C)
and a ground potential (GND), said first
resistor (R1) having its one end connected to
a first power supply potential (V.) and its
other end connected to said first node (C);

a trigger circuit (18) formed of a first switching transistor (Q1) and a second switching transistor (Q2);

said first switching transistor having its emitter connected to said first node (C); and

- said second switching transistor (Q2) having its base connected to the collector of said first switching transistor (Q1), its collector connected to the base of said first switching transistor (Q1) and to a second power supply potential (V_{α}) , and its emitter coupled to a second node (B).
 - 2. An overtemperature sensing and signaling circuit as claimed in Claim 1, wherein said resistance means comprises a positive coefficient resistor.
 - 3. An overtemperature sensing and signaling circuit as claimed in Claim 1, wherein said first switching transistor (Q1) is a bipolar transistor of the PNP-type conductivity.
 - 4. An overtemperature sensing and signaling circuit as claimed in Claim 3, wherein said second switching transistor (Q2) is a bipolar transistor of the NPN-type conductivity.

5

5

- 5. An overtemperature sensing and signaling circuit as claimed in Claim 1, wherein said voltage divider generates a turn-on signal to the emitter of said first switching transistor whenever the ambient temperature of said circuit interrupter housing exceeds a predetermined level to render said first and second switching transistors (Q1, Q2) to be conductive, thereby producing an overtemperature trip signal at the second node.
- 6. An overtemperature sensing and signaling circuit as claimed in Claim 5, further comprising a trip solenoid coil which is energized in response to said overtemperature trip signal for interrupting a current path in a circuit interrupter so as to protect the trip unit from being operated at an excessively high ambient temperature.
- 7. An overtemperature sensing and signaling circuit as claimed in Claim 1, further comprising a diode having its anode connected to the emitter of said second switching transistor (Q2) and its cathode connected to said second node (B).

15

8. An overtemperature sensing and signaling circuit for electronic trip units comprising:

thermal-responsive means (PT1) for sensing the ambient temperature within a circuit interrupter housing;

a trigger circuit (18) formed of a first switching transistor (Q1) and a second switching transistor (Q2);

said first switching transistor having

its emitter connected to said first node (C);

and

said second switching transistor (Q2) having its base connected to the collector of said first switching transistor (Q1), its collector connected to the base of said first switching transistor (Q1) and to a second power supply potential (V_{cc}) , and its emitter coupled to a second node (B).

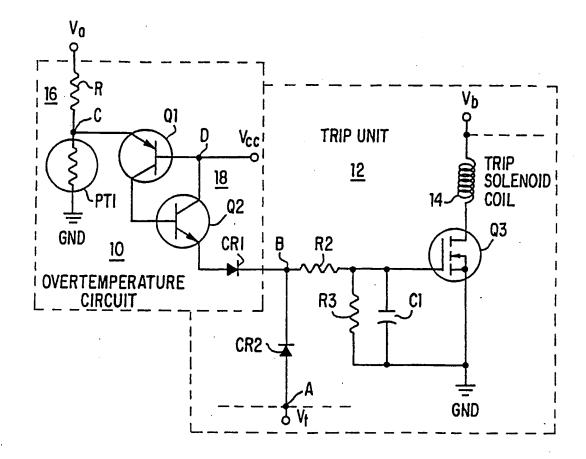
PCT/US92/10547

5

- 9. An overtemperature sensing and signaling circuit as claimed in Claim 8, wherein said thermal-responsive means comprises a positive coefficient resistor.
- 10. An overtemperature sensing and signaling circuit as claimed in Claim 8, wherein said first switching transistor (Q1) is a bipolar transistor of the PNP-type conductivity.
- 11. An overtemperature sensing and signaling circuit as claimed in Claim 10, wherein said second switching transistor (Q2) is a bipolar transistor of the NPN-type conductivity.
- 12. An overtemperature sensing and signaling circuit as claimed in Claim 8, wherein said thermal-responsive means generates a turn-on signal to the emitter of said first switching transistor whenever the ambient temperature of said circuit interrupter housing exceeds a predetermined level to render said first and second switching transistors (Q1, Q2) to be conductive, thereby producing an overtemperature trip signal at the second node.

5

- 13. An overtemperature sensing and signaling circuit as claimed in Claim 12, further comprising a trip solenoid coil which is energized in response to said overtemperature trip signal for interrupting a current path in a circuit interrupter so as to protect the trip unit from being operated at an excessively high ambient temperature.
- 14. An overtemperature sensing and signaling circuit as claimed in Claim 8, further comprising a diode having its anode connected to the emitter of said second switching transistor (Q2) and its cathode connected to said second node (B).



INTERNATIONAL SEARCH REPORT

International application No. PCT/US92/10547

### FILDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) U.S.: 361/103; 307/116; 340/584 Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) C. DOCUMENTS CONSIDERED TO BE RELEVANT Category* Citation of document, with indication, where appropriate, of the relevant passages Relevant to claim No. A U.S., A, 4,360,852 (GILMORE) 23 November 1982, See entire document. ** Special categories of cited documents: ** Comment published prior to the international filing date on the special course of particular relevance; the claimed invention c	A. CLASSIFICATION OF SUBJECT MATTER IPC(5) :HO2H 5/04 US CL :361/106; 340/593 According to International Patent Classification (IPC) or to both national classification and IPC							
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) C. DOCUMENTS CONSIDERED TO BE RELEVANT Category* Citation of document, with indication, where appropriate, of the relevant passages Relevant to claim No. A US, A, 4,360,852 (GILMORE) 23 November 1982, See entire document. * Specia categories of cited document: * Category * Category								
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) C. DOCUMENTS CONSIDERED TO BE RELEVANT Category* Citation of document, with indication, where appropriate, of the relevant passages Relevant to claim No. A US, A, 4,360,852 (GILMORE) 23 November 1982, See entire document. ** Special categories of cited documents: ** Special categor		by classification symbols)						
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) C. DOCUMENTS CONSIDERED TO BE RELEVANT Category* Citation of document, with indication, where appropriate, of the relevant passages Relevant to claim No. US, A, 4,360,852 (GILMORE) 23 November 1982, See entire Justice and the search of the documents of the search	U.S. : 361/103; 307/116; 340/584	-						
C. DOCUMENTS CONSIDERED TO BE RELEVANT Category* Citation of document, with indication, where appropriate, of the relevant passages Relevant to claim No. U.S., A., 4,360,852 (GILMORE) 23 November 1982, See entire document. Further documents are listed in the continuation of Box C. Special categories of cited documents: 'A' document defining the general state of the art which is not considered to the proof of the	Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched							
Category* Citation of document, with indication, where appropriate, of the relevant passages Relevant to claim No. U.S., A., 4,360,852 (GILMORE) 23 November 1982, See entire document. Further documents are listed in the continuation of Box C. See patent family annex. Special categories of cited documents: 'A' document defining the general state of the art which is not considered to be part of particular relevance; the claimed investion cannot be considered to be part of particular relevance; the claimed investion cannot be considered to early an an oral disclosure, use, exhibition or other special reason (as specified) To document referring to an oral disclosure, use, exhibition or other special reason (as specified) The document spublished prior to the international filing date but later than the priority date chained Date of the actual completion of the international search Date of the actual completion of the internati	Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)							
Category* Citation of document, with indication, where appropriate, of the relevant passages Relevant to claim No. U.S., A., 4,360,852 (GILMORE) 23 November 1982, See entire document. Further documents are listed in the continuation of Box C. See patent family annex. Special categories of cited documents: 'A' document defining the general state of the art which is not considered to be part of particular relevance; the claimed investion cannot be considered to be part of particular relevance; the claimed investion cannot be considered to early an an oral disclosure, use, exhibition or other special reason (as specified) To document referring to an oral disclosure, use, exhibition or other special reason (as specified) The document spublished prior to the international filing date but later than the priority date chained Date of the actual completion of the international search Date of the actual completion of the internati	•							
See patent family annex. See patent family annex.	C. DOCUMENTS CONSIDERED TO BE RELEVANT							
Further documents are listed in the continuation of Box C. See patent family annex. * Special categories of cited documents: document defining the general state of the art which is not considered to be part of particular relevance * Course the part of particular relevance * Course the part of particular relevance (active of particular relevance) * Course the document published on a rafter the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory unique the between the document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is taken alone document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents is combined with one or or more other such documents in the priority date claimed *2.* document of particular relevance; the claimed invention cannot be considered to involve the document is alter alone document of particular relevance; the claimed invention cannot be considered to involve or more other such documents is combined with one or or more other such documents is combined with one or or more other such documents is combined with one or or more other such documents is combined with one or or more other such documents are being obvious to a pert skilled in the art of the same patent family *2.* document of particular relevance; the claimed invention cannot be considered to involve the adventure is taken alone document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or or more other such documents are combined with one or or more other such documents are combined with one or or more other such documents are combined with one or or more other such docu	Category* Citation of document, with indication, where ap	ppropriate, of the relevant passages Relevant to claim No.						
Special categories of cited documents: "A" document defining the general state of the art which is not considered to be part of particular relevance "E" earlier document published on or after the international filling date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filling date but later than the priority date claimed "Date of the actual completion of the international search "T" later document published after the international filling date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "X" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "A" document published prior to the international filing date but later than the priority date claimed Date of the actual completion of the international search Date of the actual completion of the international search		November 1982, See entire 1-14						
Special categories of cited documents: "A" document defining the general state of the art which is not considered to be part of particular relevance "E" earlier document published on or after the international filling date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filling date but later than the priority date claimed "Date of the actual completion of the international search "T" later document published after the international filling date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "A" document published prior to the international filling date but later than the priority date claimed Date of the actual completion of the international search Date of the actual completion of the international search								
Special categories of cited documents: "A" document defining the general state of the art which is not considered to be part of particular relevance "E" earlier document published on or after the international filling date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filling date but later than the priority date claimed "Date of the actual completion of the international search "T" later document published after the international filling date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "X" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "A" document published prior to the international filing date but later than the priority date claimed Date of the actual completion of the international search Date of the actual completion of the international search	·							
Special categories of cited documents: "A" document defining the general state of the art which is not considered to be part of particular relevance "E" earlier document published on or after the international filling date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filling date but later than the priority date claimed "Date of the actual completion of the international search "T" later document published after the international filling date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "X" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "A" document published prior to the international filing date but later than the priority date claimed Date of the actual completion of the international search Date of the actual completion of the international search								
Special categories of cited documents: "A" document defining the general state of the art which is not considered to be part of particular relevance "E" earlier document published on or after the international filling date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filling date but later than the priority date claimed "Date of the actual completion of the international search "T" later document published after the international filling date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "X" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "A" document published prior to the international filing date but later than the priority date claimed Date of the actual completion of the international search Date of the actual completion of the international search								
Special categories of cited documents: "A" document defining the general state of the art which is not considered to be part of particular relevance "E" earlier document published on or after the international filling date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filling date but later than the priority date claimed "Date of the actual completion of the international search "T" later document published after the international filling date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "A" document published prior to the international filling date but later than the priority date claimed Date of the actual completion of the international search Date of the actual completion of the international search	·							
Special categories of cited documents: "A" document defining the general state of the art which is not considered to be part of particular relevance "E" earlier document published on or after the international filling date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filling date but later than the priority date claimed "A" later document published after the international filling date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "A" document published prior to the international filing date but later than the priority date claimed "Date of the actual completion of the international search "Date of the actual completion of the international search								
Special categories of cited documents: "A" document defining the general state of the art which is not considered to be part of particular relevance "E" earlier document published on or after the international filling date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filling date but later than the priority date claimed "A" later document published after the international filling date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "A" document published prior to the international filing date but later than the priority date claimed "Date of the actual completion of the international search "Date of the actual completion of the international search								
Special categories of cited documents: "A" document defining the general state of the art which is not considered to be part of particular relevance "E" earlier document published on or after the international filling date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filling date but later than the priority date claimed "Date of the actual completion of the international search "T" later document published after the international filling date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "X" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "A" document published prior to the international filing date but later than the priority date claimed Date of the actual completion of the international search Date of the actual completion of the international search								
Special categories of cited documents: "A" document defining the general state of the art which is not considered to be part of particular relevance "E" earlier document published on or after the international filling date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filling date but later than the priority date claimed "Date of the actual completion of the international search "T" later document published after the international filling date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "X" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "A" document published prior to the international filing date but later than the priority date claimed Date of the actual completion of the international search Date of the actual completion of the international search								
'A' document defining the general state of the art which is not considered to be part of particular relevance "E' earlier document published on or after the international filing date "L' document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O' document referring to an oral disclosure, use, exhibition or other means "P' document published prior to the international filing date but later than the priority date claimed Date of the actual completion of the international search "A' date and not in conflict with the application but cited to understand the principle or theory underlying the invention cannot be considered novel or cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art document member of the same patent family Date of the actual completion of the international search Date of the actual completion of the international search	Further documents are listed in the continuation of Box C	See patent family annex.						
"E" earlier document published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed Date of the actual completion of the international search "X" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art document member of the same patent family	"A" document defining the general state of the art which is not considered	date and not in conflict with the application but cited to understand the						
cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed Date of the actual completion of the international search "V" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art document member of the same patent family Date of the actual completion of the international search Date of the actual completion of the international search	"E" earlier document published on or after the international filing date	considered novel or cannot be considered to involve an inventive step						
"O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed Date of the actual completion of the international search Considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art document member of the same patent family Date of the actual completion of the international search Date of the actual completion of the international search	cited to establish the publication date of another citation or other							
the priority date claimed Date of the actual completion of the international search Date of mailing of the priority date claimed Date of the actual completion of the international search Date of mailing of the priority date claimed	"O" document referring to an oral disclosure, use, exhibition or other	considered to involve an inventive step when the document is combined with one or more other such documents, such combination						
31 MARCH 1993		Date of maiting of 1999 mational search report						
Name and mailing address of the ISA/US Commissioner of Patents and Trademarks Authorized officer Authorized officer	Commissioner of Patents and Trademarks	,(,, , , , , , , , , , , , , , , , ,						
Box PCT Washington, D.C. 20231 Foocimile No. NOT APPLICABLE TODD E. DE BOER Telephone No. (703) 308-1678	Washington, D.C. 20231	TODD E. DE BOER Telephone No. (703) 308-1678						